Appendix C – 6502 Instruction Set Summary

ADC   Add Memory to Accumulator with Carry
AND   'AND' Memory with Accumulator
ASL   Shift Left one bit (Memory or Accumulator)

BCC   Branch on Carry Clear
BCS   Branch on Carry Set
BEQ   Branch on result Zero
BIT   Test bits in Memory with Accumulator
BMI   Branch on result Minus
BNE   Branch on result not Zero
BPL   Branch on result Plus
BRK   Force Break
BVC   Branch on Overflow Clear
BVS   Branch on Overflow Set

CLC   Clear Carry flag
CLD   Clear Decimal mode
CLI   Clear Interrupt disable bit
CLV   Clear Overflow flag
CMP   Compare Memory and Accumulator
CPX   Compare Memory and index X
CPY   Compare Memory and index Y

DEC   Decrement Memory by one
DEX   Decrement index X by one
DEY   Decrement index Y by one

EOR   'Exclusive-OR' Memory with Accumulator

INC   Increment Memory by one
INX   Increment index X by one
INY   Increment index Y by one

JMP   Jump to new location
JSR   Jump to subroutine

LDA   Load Accumulator with Memory
LDX   Load index X with Memory
LDY   Load index Y with Memory
LSR   Shift one bit right (Memory or Accumulator)

NOP	No operation
ORA	'OR' Memory with Accumulator
PHA	Push Accumulator on Stack
PHP	Push Processor Status on Stack
PLA	Pull Accumulator from Stack
PLP	Pull Processor Status from Stack
ROL	Rotate one bit left (Memory or Accumulator)
ROR	Rotate one bit right (Memory or Accumulator)
RTI	Return from Interrupt
RTS	Return from subroutine
SBC	Subtract Memory from Accumulator with Carry
SEC	Set Carry flag
SED	Set Decimal mode
SEI	Set Interrupt disable status
STA	Sore Accumulator in Memory
STX	Store index X in Memory
STY	Store index Y in Memory
TAX	Transfer Accumulator to index X
TAY	Transfer Accunulator to index Y
TSX	Transfer Stack Pointer to index X
TXA	Transfer index X to Accumulator
TXS	Transfer index X to Stack Register
TYA	Transfer index Y to Accumulator